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(A)) Page 1 of 3 LAB A-03 (Week 4)  
Analysis of Combinational Logic  
Circuits Objectives 1. To obtain  
Boolean expression for the output  
based on the given logic diagram 2. To  
simplify the logic circuit using Boolean*

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*algebra techniques, construct the simplified circuit and verify the truth table for the simplified expression.*

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[What is Digital Logic? - Definition from Techopedia](#)

*Computer Logical Organization refers to the level of abstraction above the digital logic level, but below the*



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*operating system level. At this level, the major components are functional units or subsystems that correspond to specific pieces of hardware built from the lower level building blocks.*

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*Applications*

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*In digital logic, the inputs and output of a function are in the form of binary numbers (boolean values) i.e., the*

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*values are either zero (0) or one (1).  
Therefore, digital logic is also known  
as 'Boolean logic'.*

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